## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Kiichi HIRANO, et al.

Serial No.:

09/813,866

Filed:

March 22, 2001

For:

1.116

November 17, 2003 SEMICONDUCTOR DEVICE DISPLAY AND METHOD OF

FABRICATING THE SAME

AMENDMENT UNDER 37 C.F.R. § 1.116

**Introductory Comments** 

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated June 16, 2003, the period for response being extended from September 16, 2003, until November 16, 2003, (November 16, 2003 falls on a Sunday) by the attached Petition for Extension of Time, please amend the aboveidentified application as set forth below.

Confirmation No.: 6213 Art Unit: 2811

Examiner: S. Gebremariam

Docket No.: 107318-00000